

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) A method of manufacturing a large EL panel which includes a plurality of EL display panels, each of the plurality of EL display panels being formed of an EL display device and a sub-transparent substrate that supports the EL display device in parallel, the EL display device including: a base layer over which a luminescent material is applied; an electrode layer which is laminated on one side of the base layer; and a TFT layer including a circuit section in which light emission of the luminescent material is controlled by applying a predetermined voltage between the electrode layer and the TFT layer and a pixel section which is superimposed over another side of the base layer opposite the one side and which is provided with a plurality of pixels which divide the base layer into sections so that light emission of the luminescent material in each section is individually controlled by generating a potential difference between the electrode layer and the TFT layer at the corresponding section, the method of manufacturing comprising:

removing the EL display devices from the sub transparent substrates;

arranging the obtained EL display devices on a main transparent substrate, which is broader than the sub-transparent substrate, in a matrix pattern and in a manner such that light emitting areas of the adjacent EL display devices are in proximity to each other; and

disposing and fixing the circuit section of the TFT layer, which is disposed at a region outside the light emitting area, behind an adjacent EL display device.

2. (Previously Presented) The manufacturing method for a large EL panel according to Claim 1, the fixing including fixing the circuit section of the TFT layer in a manner such that the TFT layer is bent at a boundary between the circuit section, and the pixel section and the circuit section is disposed behind an adjacent EL display device.

3. (Original) The manufacturing method for a large EL panel according to Claim 1, further including changing a thickness of an adhesive layer used to laminate the EL display devices on said main transparent substrate so as to form a step portion which changes positions of the adjacent EL display devices in a thickness direction, so that said circuit section is disposed behind an adjacent EL display device.

4. (Original) The manufacturing method for a large EL panel according to Claim 1, further including inverting adjacent EL display devices, and increasing a thickness of said pixel portion of said TFT layer so as to compensate for displacement between the base layers of adjacent EL display devices, which is caused as a result of the inverting steps, so that said circuit section is disposed behind the adjacent EL display device.

5. (Previously Presented) A manufacturing method of an EL panel comprising:  
providing a substrate;  
disposing a first EL display element including a TFT layer above the substrate, the TFT layer including a plurality of first pixel sections that are arranged in the first EL display element and a first circuit section, each of the first pixel sections being capable of emitting lights, and the first circuit section controlling the emissions of the first pixel sections; and

disposing a second EL display element including a TFT layer above the substrate so as to be arranged adjacent to the first EL display element, the TFT layer including a plurality of second pixel sections are arranged in the second EL display element, each of the second pixel sections being capable of emitting lights, the first circuit section and the second EL display element overlapping with each other such that a pitch between adjacent ones of the first pixel sections and the second pixel sections being approximately constant with a pitch between adjacent ones of the first pixel sections.

6. (Previously Presented) The manufacturing method of an EL panel according to claim 5, a second circuit section controlling the emissions of the second pixel sections, the method further comprising:

disposing a third EL display element that overlaps with the second circuit section, a plurality of third pixel sections arranged in the third EL element, each of the third pixel sections being capable of emitting lights,

wherein a pitch between adjacent ones of the second pixel sections and the third pixel sections is approximately constant with at least one of a pitch between adjacent ones of the first pixel sections and a pitch between adjacent ones of the second pixel sections.

7. (Original) The manufacturing method of an EL panel according to claim 5, wherein the second EL display element emits light from a first side, and wherein the second EL element overlaps with the first circuit section at a second side opposite to the first side.

8. (Original) The manufacturing method of an EL panel according to claim 5, said substrate being transparency and supporting said second EL display element from said second side thereof.

9. (Original) The manufacturing method of an EL panel according to claim 8, wherein said first EL display element and said second EL element are adjoined by adhesive material.

10. (Previously Presented) The manufacturing method of an EL panel according to claim 5, further comprising:

connecting the first circuit section and the first pixel sections to each other;

and

bending the first circuit section such that the first circuit section overlaps with the second EL display element.

11. (Previously Presented) The manufacturing method of an EL panel according to claim 5, further comprising:

forming a layer that fills a gap formed between the second EL display element and the first circuit section.

12. (Original) The manufacturing method of an EL according to claim 5, wherein the thickness of the first circuit section is thinner than that of the first pixel sections.

13. (Canceled)

14. (Canceled)

15. (Canceled)

16. (Canceled)

17. (Canceled)

18. (Canceled)